

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S53	20	(layout with schematic with (compar\$3 verif\$7 check\$3) with width with (line conductor path trace wire wiring))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 11:12
S55	243	((compar\$5 verif\$7 check \$3) with width with (line conductor path trace wire wiring)) and (layout same schematic)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 11:27
S54	10605	((compar\$3 verif\$7 check \$3) with width with (line conductor path trace wire wiring))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 11:27
S56	136	((compar\$5 verif\$7 check \$3) with width with (line conductor path trace wire wiring)) and (layout same schematic) and (@ad<"20020719" or @riad<"20020719")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/07 11:29
L1	135	(((identif\$7 indicat\$3 annotat \$3 footnot\$3 signify\$3 denot \$3 specify\$3 tag\$4 flag\$4 mark\$3 determin\$5 label\$3 tabbing tab) with (width) with (lin\$3 conductor wir\$3) with (minimum tolera\$4 limit \$3 specify\$3 specified specificat\$3 great\$2 small \$3)) and (((schematic with (layout (lay adj out)) with (check\$3 verif\$7 compar\$3 test\$3)) lvs)) and ((indicat \$3 annotat\$3 footnot\$3 record\$3 report\$3 signify\$3 denot\$3 tag\$4 flag\$4 mark \$3 label\$3 tabbing tab alert \$3 alarm\$3 notif\$7 warn\$3) with (error mistake deviat\$3 delinquenc\$3 failure fals\$3 inaccurat\$3 flaw width) with (width))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/11 19:04

L2	7448	((tag\$4 flag\$4 mark\$3 annotat\$3 label\$3) with (width) with (line conductor wire wiring))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/11 19:19
L3	268	(((schematic with (layout (lay adj out)) with (check\$3 verif\$7 compar\$3 test\$3)) lvs)) same ((line wire wiring conductor) with width)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/11 19:26
L4	383	((((schematic netlist (net adj list)) with (layout (lay adj out)) with (check\$3 compar \$3 convert\$3 verify\$3 verified verificat\$3 test\$3)) lvs)) same ((lin\$3 wir\$3 conductor) with width)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/11 19:42
L5	96	((drc (rule near2 check\$3) verif\$7 check\$3 validat\$3 identif\$7) with (width) with (lin\$3 wir\$3 conductor) with (minimum smallest toler\$4 suitable conform\$4 allow\$4 least)) and ((((schematic design netlist (net adj list)) with (layout (lay adj out)) with (check\$3 compar\$3 convert\$3 verify\$3 verified verificat\$3 test\$3)) lvs))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/11 19:44
L6	110	((schematic netlist (net adj list)) with (check\$3 drc verif \$7)) and ((wir\$3 lin\$3 conductor) with (width) near4 (smallest minimum lowest least)) and ((((schematic netlist (net adj list)) with (layout (lay adj out)) with (check\$3 compar \$3 convert\$3 verify\$3 verified verificat\$3 test\$3)) lvs))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/11 19:57
L7	21	(schematic and layout and minimum and width).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/11 19:58

5/11/2008 8:11:30 PM

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